

PEVA

XTX Evaluation Backplane

Rev. A.0

Content

Page 1	MAIN
Page 2	XTX Connector X1
Page 3	PCI Bus
Page 4	USB Bus and Sound
Page 5	PCI Post
Page 6	XTX Connector X2
Page 7	SATA
Page 8	PCI Express
Page 9	Express Card
Page 10	LPC Super IO
Page 11	LPC FWs
Page 12	AC97
Page 13	XTX Connector X3
Page 14	VGA TV
Page 15	LPT COM FLPY MB MS
Page 16	LCD
Page 17	XTX Connector X4
Page 18	IDE and Ethernet
Page 19	PWR BATT SPK Feature FAN
Page 20	DCDC 3.3V
Page 21	Revision History

Variants

Base Components used on all options

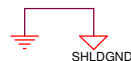
isolated frame GND for 1.5V DCDC
page 10



isolated frame GND for 3.3V SB DCDC
page 8



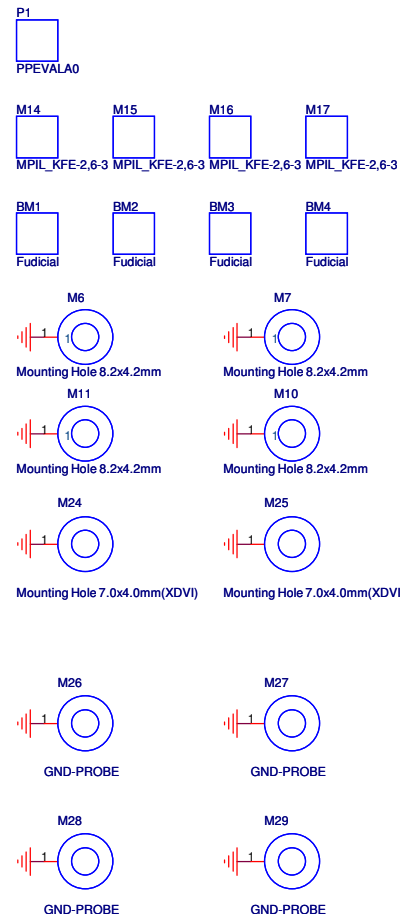
isolated frame GND for connectors




isolated frame GND for 3.3V DCDC
page 19



isolated frame GND for Audio codec
page 12



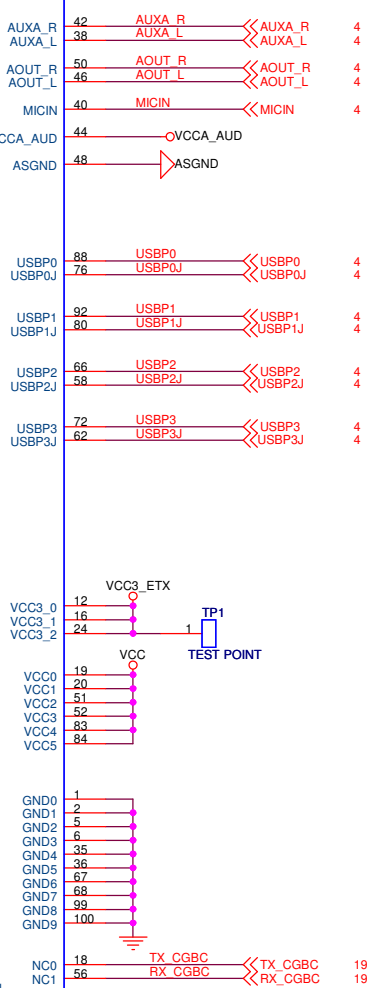
Congatec AG Auwiesenstrasse 5 D-94469 Deggendorf Germany		 congatec the rhythm of embedded computing	
Title PEVA - XTX pro Evaluation Backplane			
Size B	Document Number PEVASA0	Created	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 1	of 21

3.5 PCI_AD[0..31] << PCI_AD[0..31]


PCI

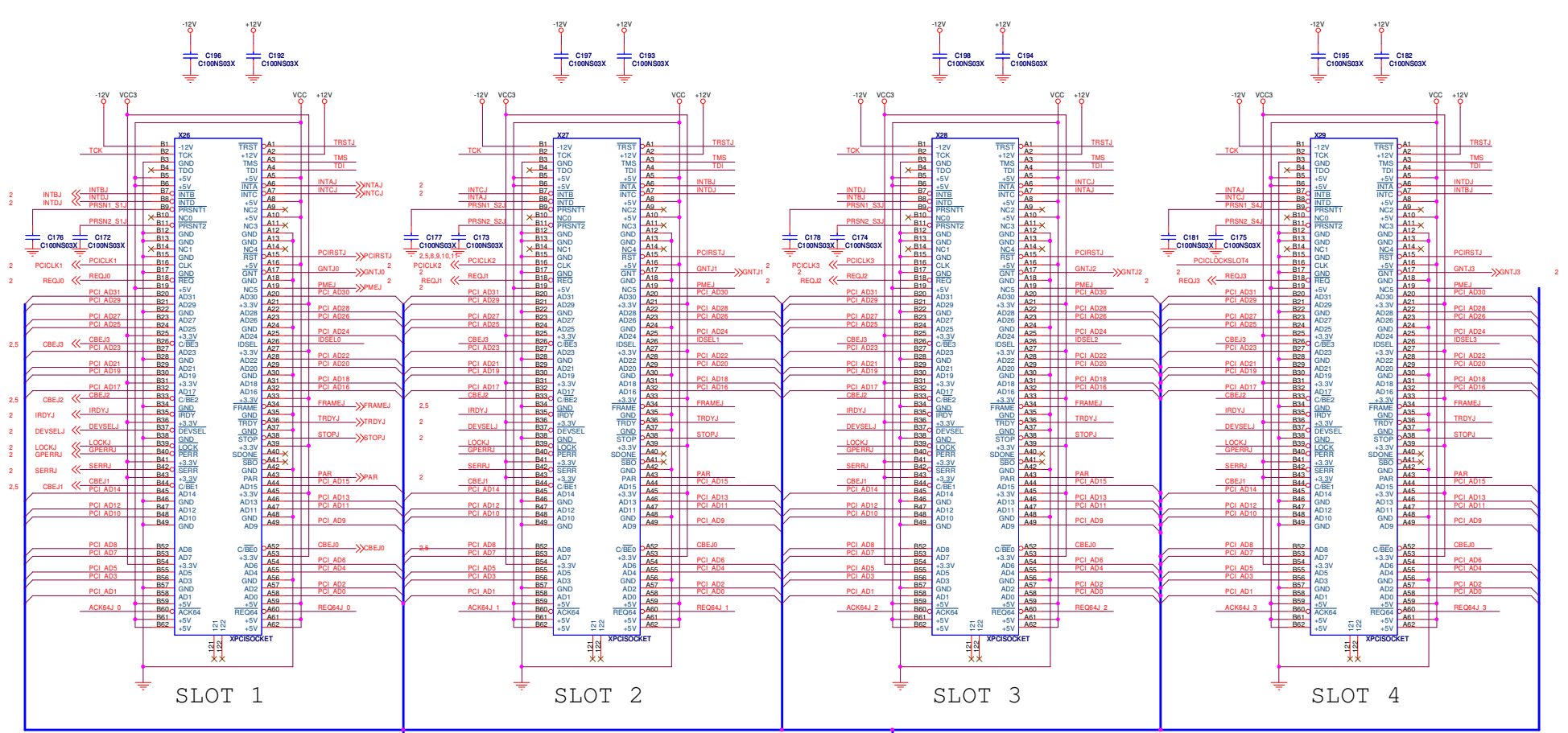
3.5	CBEJ0	<<	CBEJ0	31
3.5	CBEJ1	<<	CBEJ1	49
3.5	CBEJ2	<<	CBEJ2	70
3.5	CBEJ3	<<	CBEJ3	82
3	PAR	<<	PAR	53
3	GPERRJ	<<	GPERRJ	55
3	PMEJ	<<	PMEJ	57
3	LOCKJ	<<	LOCKJ	59
3	TRDYJ	<<	TRDYJ	61
3	IRDYJ	<<	IRDYJ	63
3.5	FRAMEJ	<<	FRAMEJ	65
3	DEVSELJ	<<	DEVSELJ	66
3	SERRJ	<<	SERRJ	54
3	STOPJ	<<	STOPJ	64
3.5,8,9,10,11	PCIRSTJ	<<	PCIRSTJ	93
3	REQJ0	<<	REQJ0	22
3	REQJ1	<<	REQJ1	15
3	REQJ2	<<	REQJ2	13
3	REQJ3	<<	REQJ3	9
3	GNTJ0	<<	GNTJ0	17
3	GNTJ1	<<	GNTJ1	14
3	GNTJ2	<<	GNTJ2	11
3	GNTJ3	<<	GNTJ3	10
3	INTAJ	<<	INTAJ	97
3	INTBJ	<<	INTBJ	98
3	INTCJ	<<	INTCJ	95
3	INTDJ	<<	INTDJ	96
3	PCICLK1	<<	PCICLK1	7
3	PCICLK2	<<	PCICLK2	8
3	PCICLK3	<<	PCICLK3	3
3	PCICLK4	<<	PCICLK4	4
10,19	SERIRQ	<<	SERIRQ	21

X1	
AD0	42
AD1	38
AD2	
AD3	
AD4	50
AD5	46
AD6	
AD7	40
AD8	
AD9	44
AD10	
AD11	48
AD12	
AD13	
AD14	
AD15	
AD16	
AD17	
AD18	
AD19	
AD20	
AD21	
AD22	
AD23	
AD24	
AD25	
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AD28	
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AD31	
CBEJ0	31
CBEJ1	49
CBEJ2	70
CBEJ3	82
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GPERRJ	55
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IRDYJ	63
FRAMEJ	65
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REQJ3	9
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GNTJ2	11
GNTJ3	10
INTAJ	97
INTBJ	98
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PCICLK1	7
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SERIRQ	21

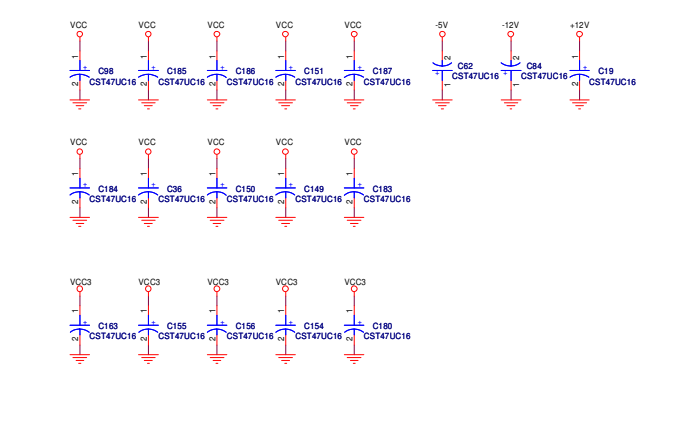
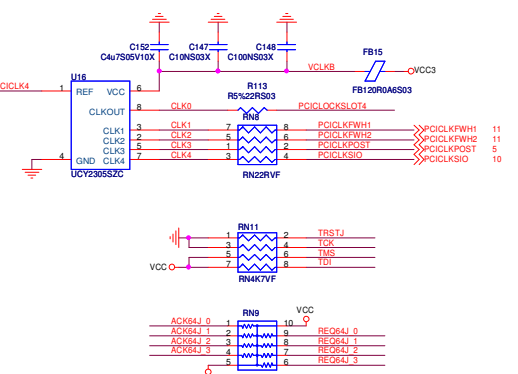
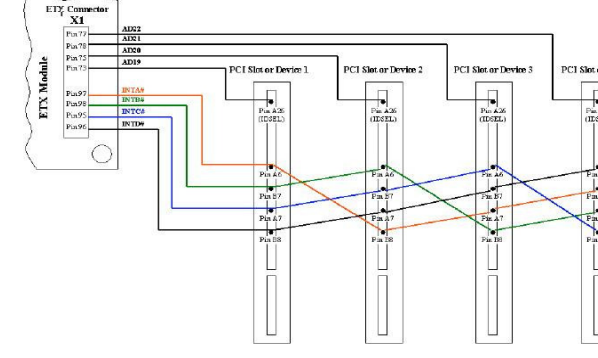


ETX Connector X1
PCI Bus / Sound / USB

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Size B	Document Number: PEVASA0	Created	Rev A.0
Date: Wednesday, April 05, 2006	Sheet 2	of 21	



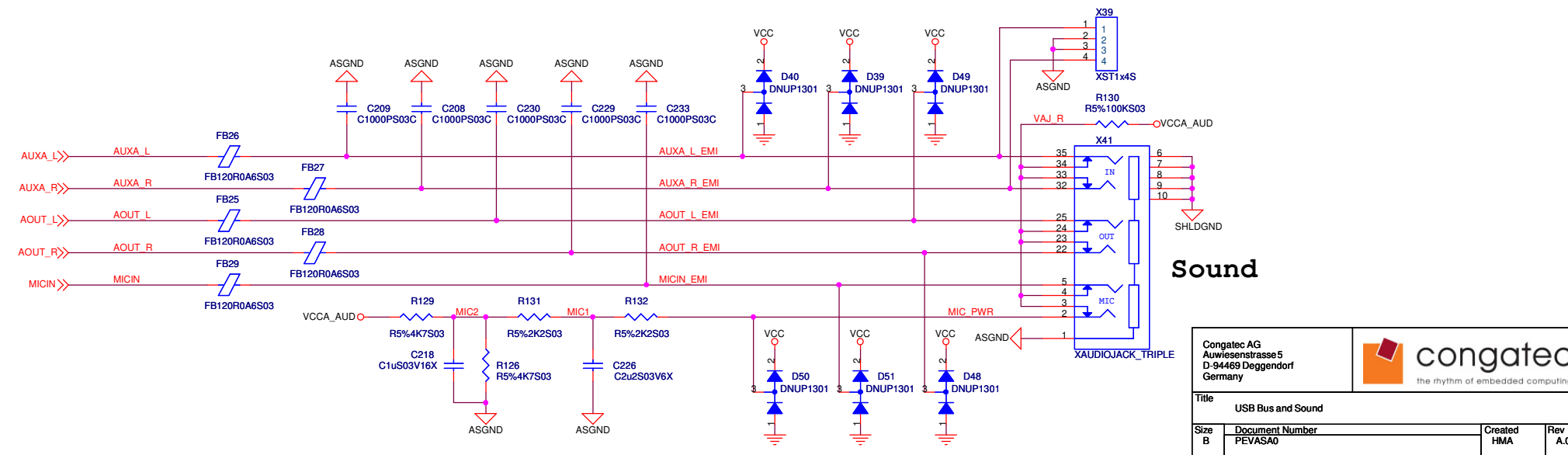
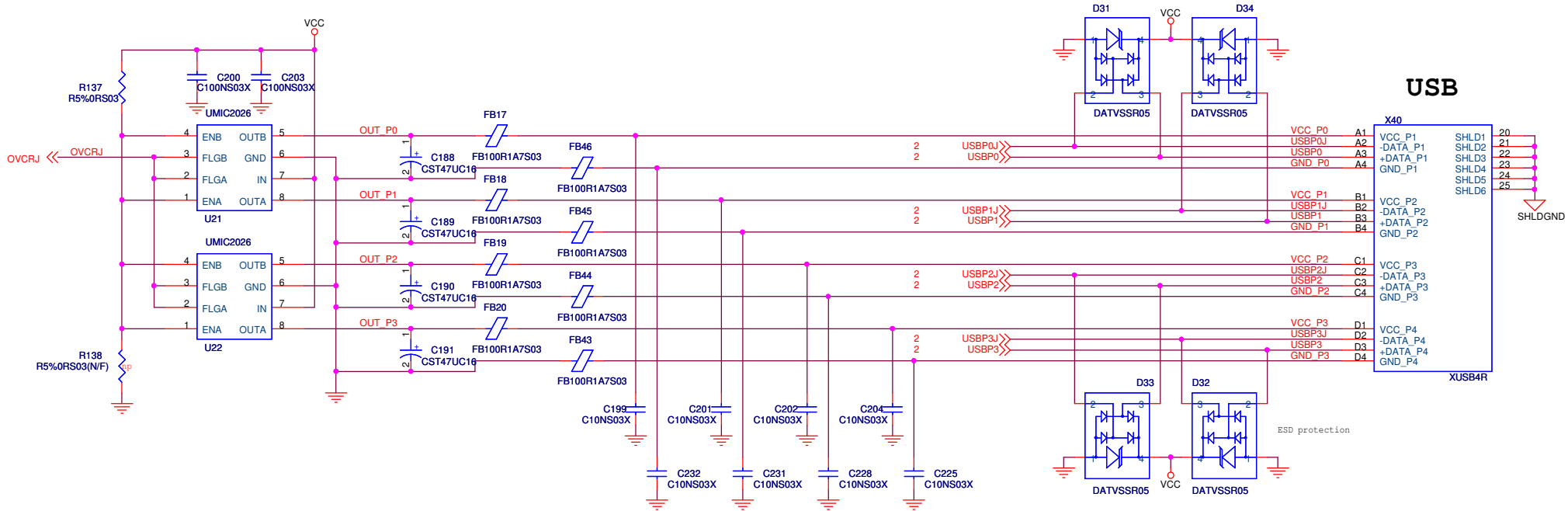
ETX pro - PCI interrupt routing and IDSELS



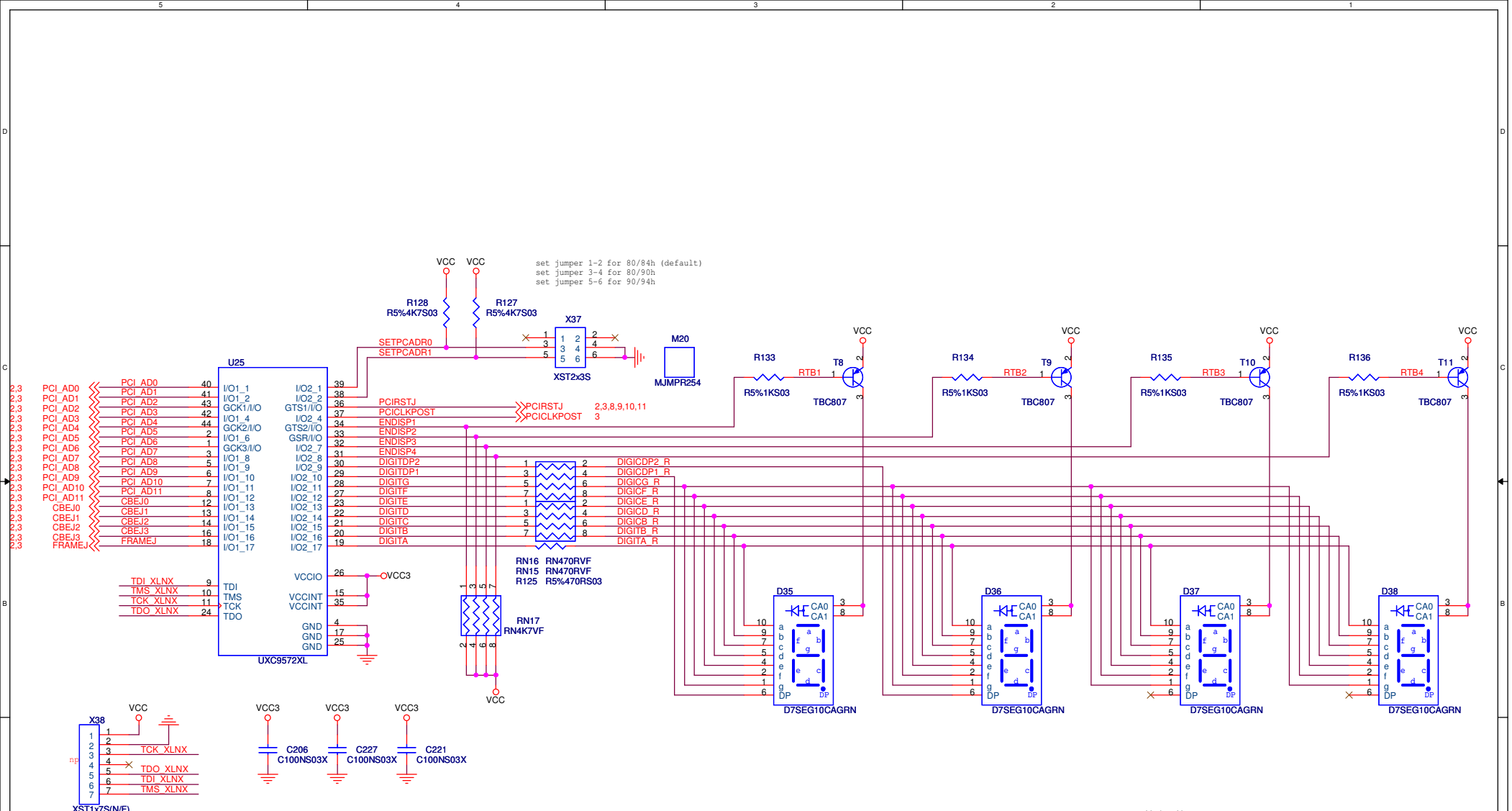
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 Awarenessstr. 5
 D-94469 Dingolfing
 Germany

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Date	Wednesday, April 25, 2006	Created	HMA
		Rev	A.0
Page 3		of 21	

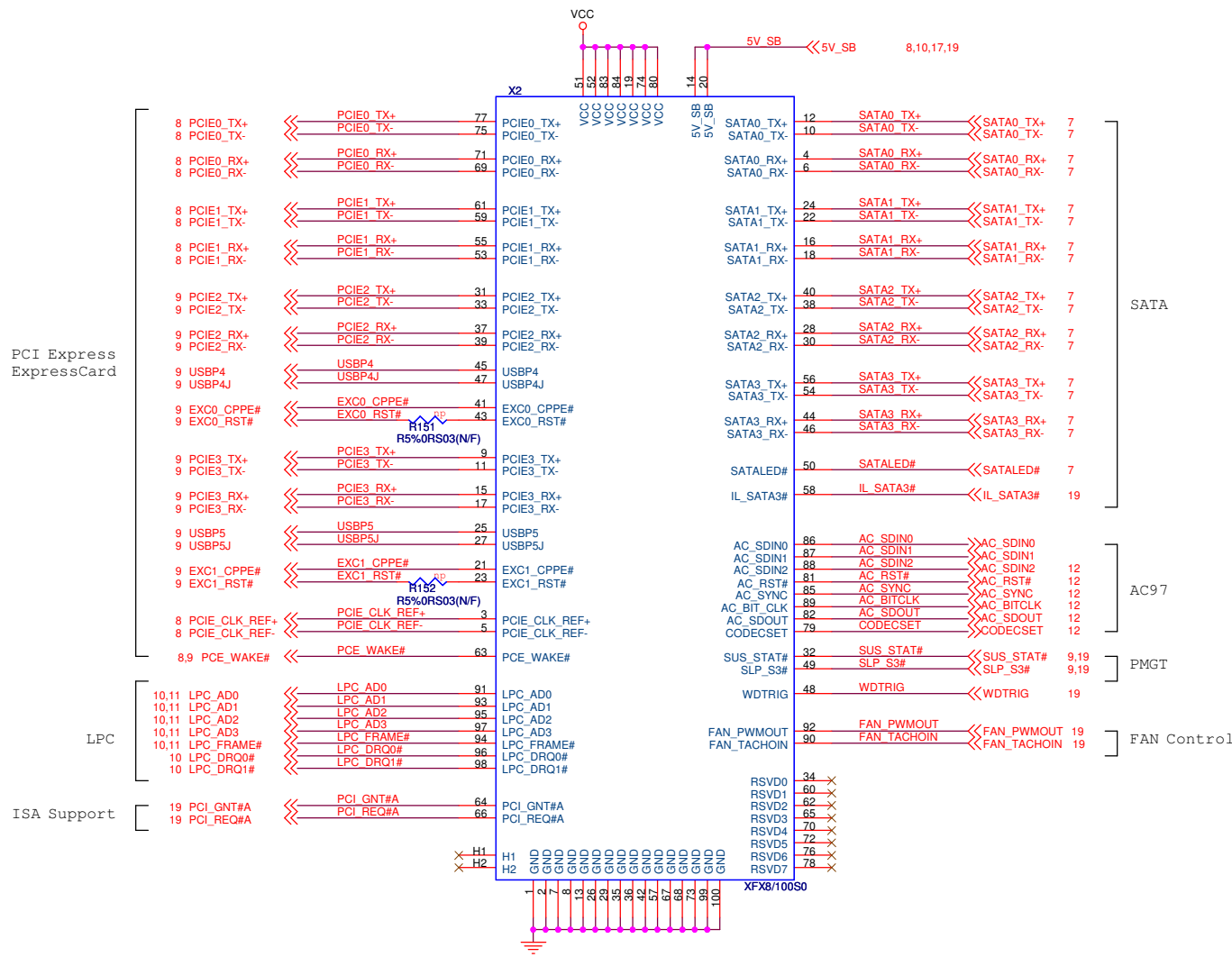


congatec <small>the rhythm of embedded computing</small>		congatec <small>the rhythm of embedded computing</small>	
Congatec AG Auwiesenstrasse 5 D-94469 Deggendorf Germany			
Title USB Bus and Sound			
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date: Wednesday, April 05, 2006	Sheet 4	of 21	




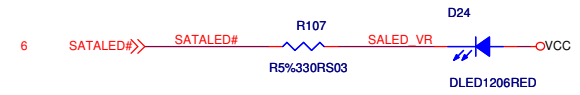
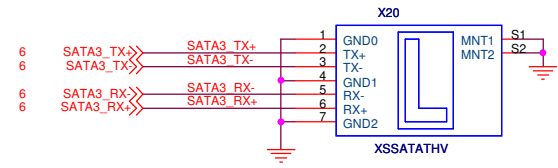
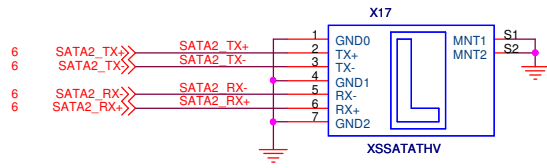
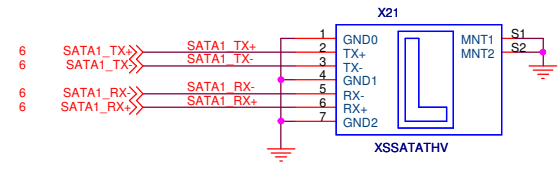
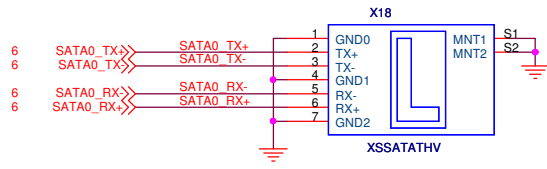
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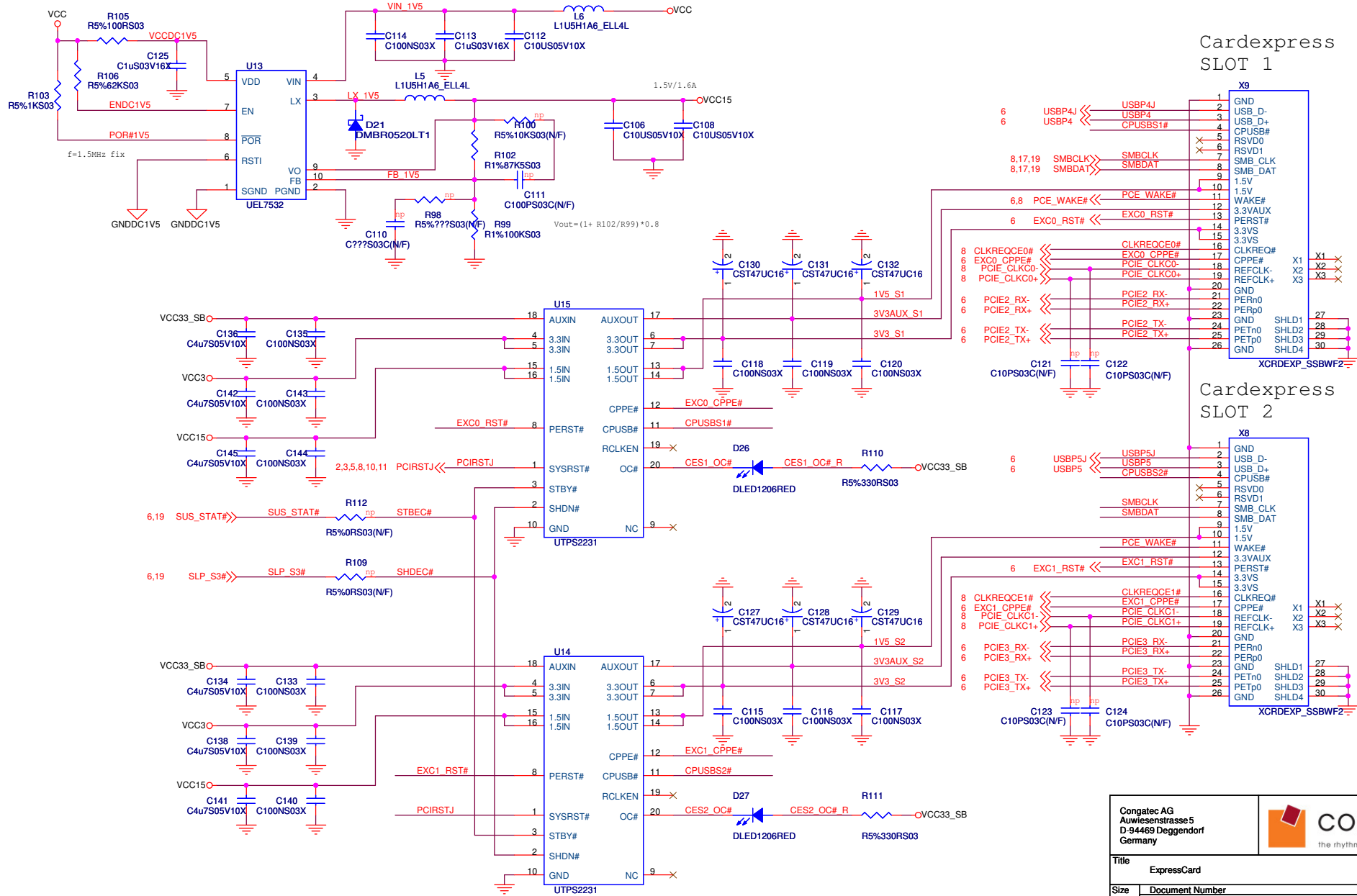
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Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 5	of 21



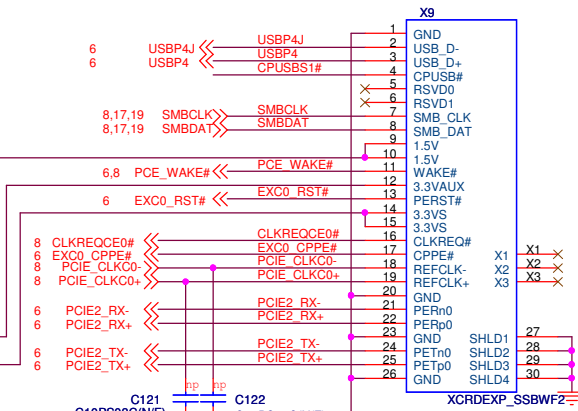
XTX Connector X2
 PCI Express / SATA / AC97 / LPC

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Date: Wednesday, April 05, 2006	Sheet: 6	of 21	

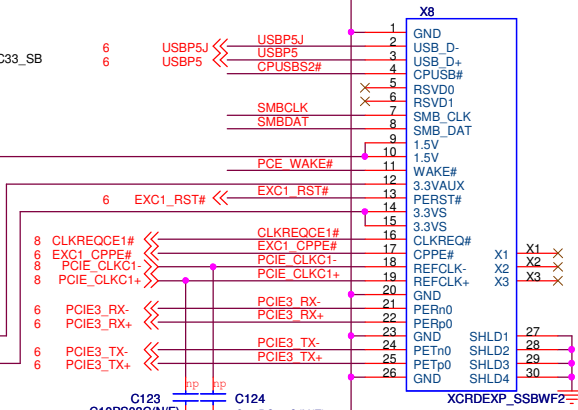





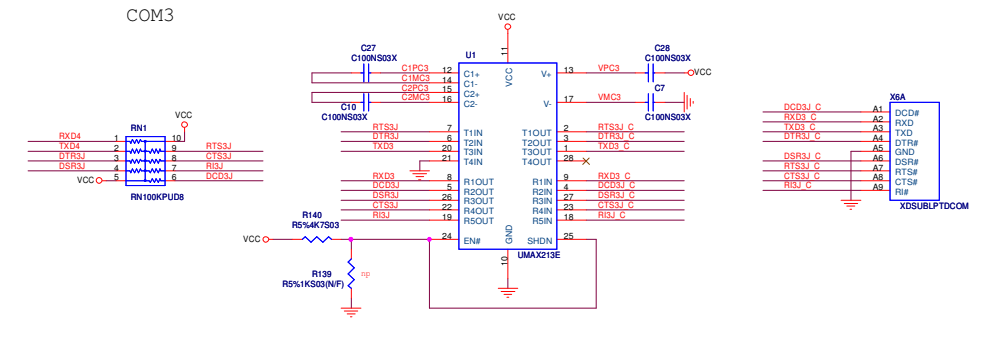
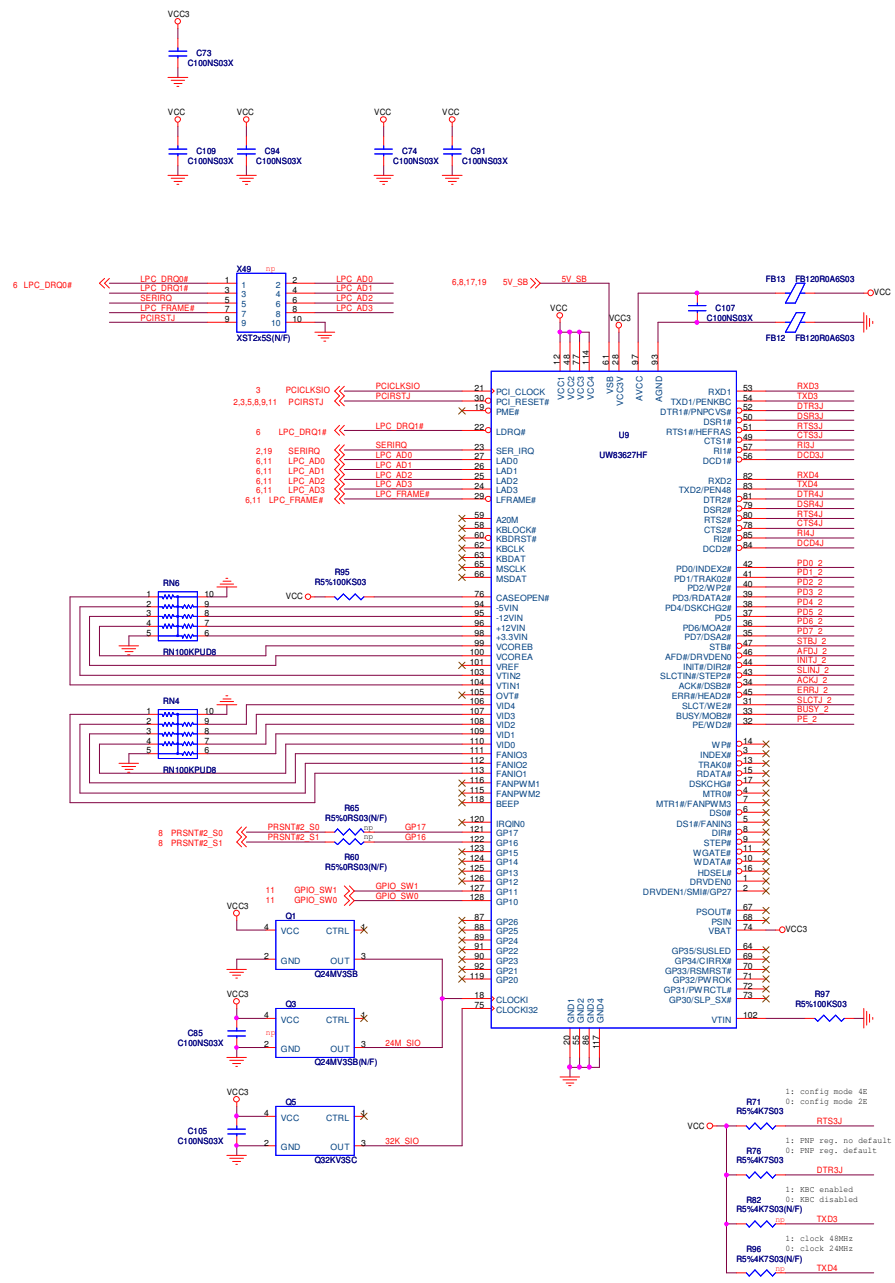
Cardexpress SLOT 1



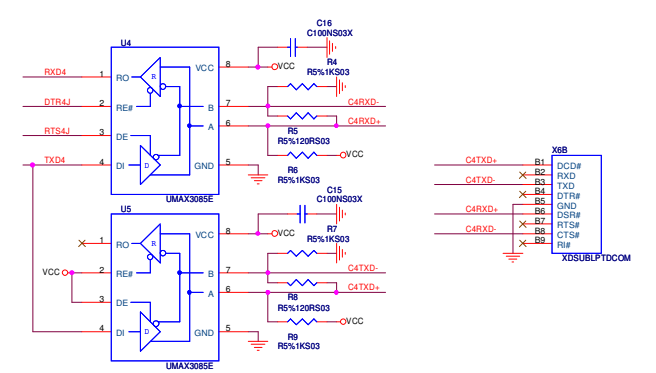
Cardexpress SLOT 2



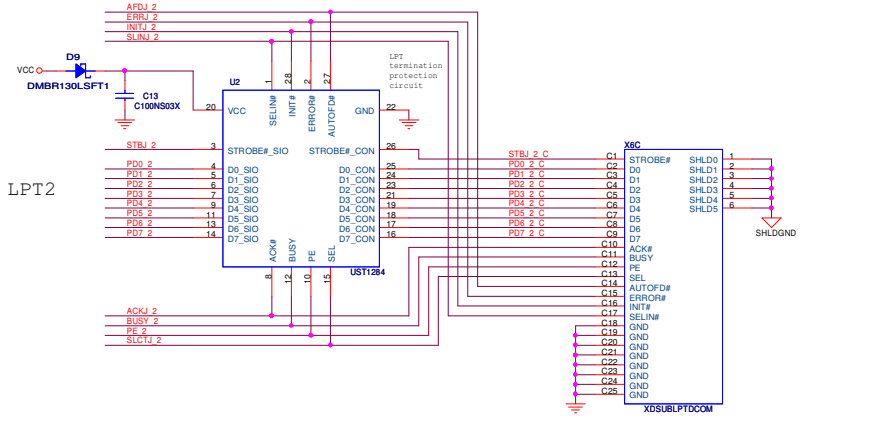
Congatec AG Auwiesenstrasse 5 D-94469 Deggendorf Germany		 the rhythm of embedded computing	
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Date: Wednesday, April 05, 2006	Sheet: 9	of 21	



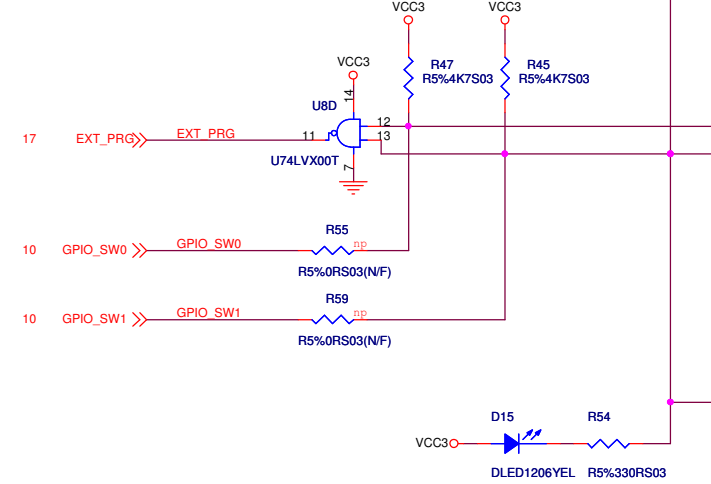
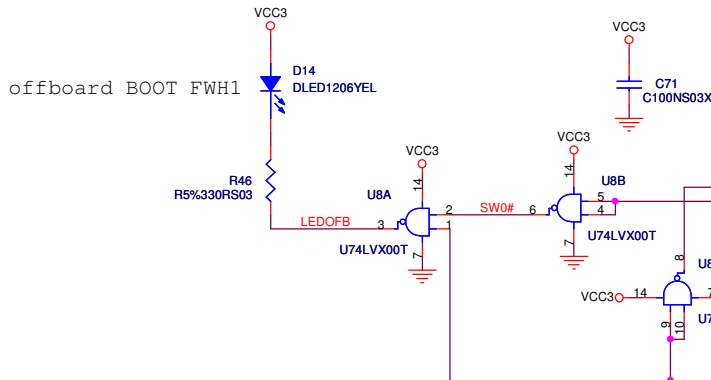
COM4 (RS485/RS422)



LPT2

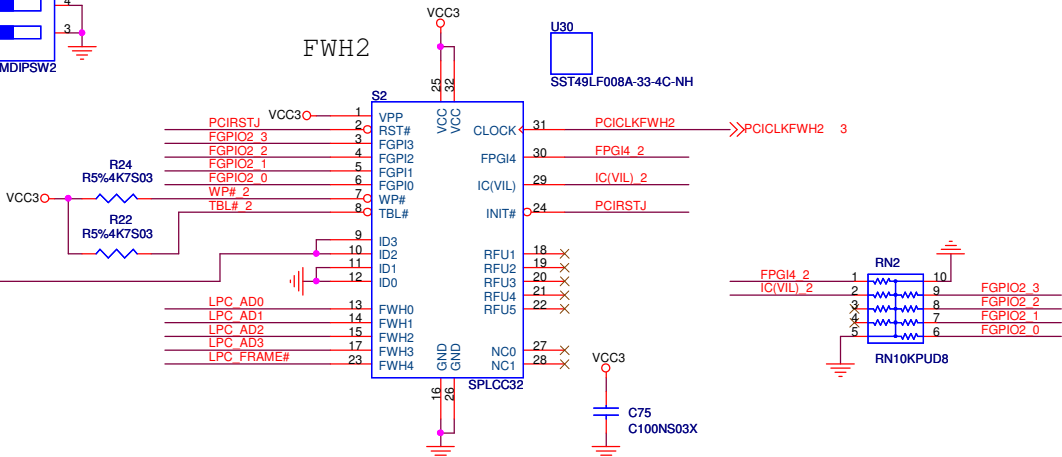
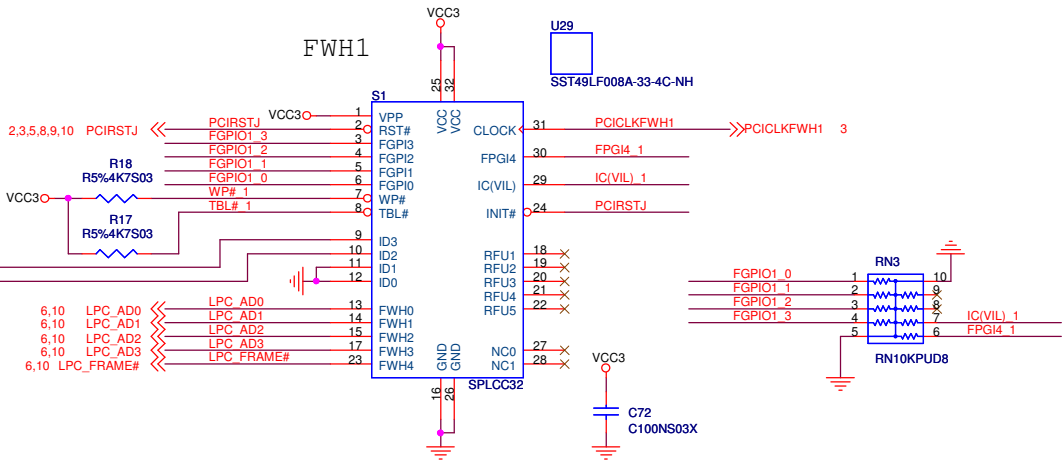


offboard BOOT FWH1



offboard BOOT FWH2

both off: BOOT from onboard FWH
 set 1: BOOT from offboard FWH1
 set 2: BOOT from offboard FWH2

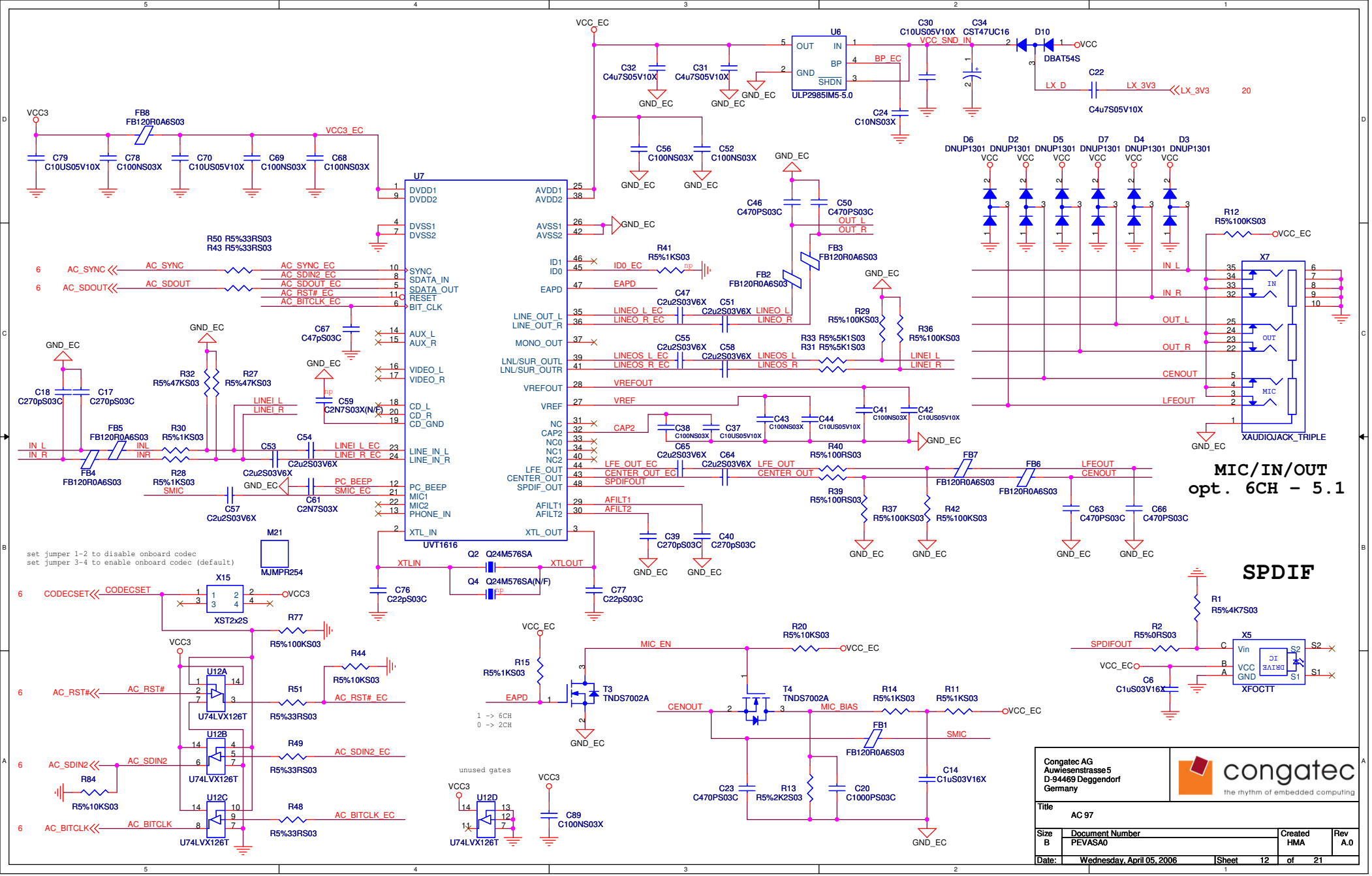


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Title: LPC FWHs

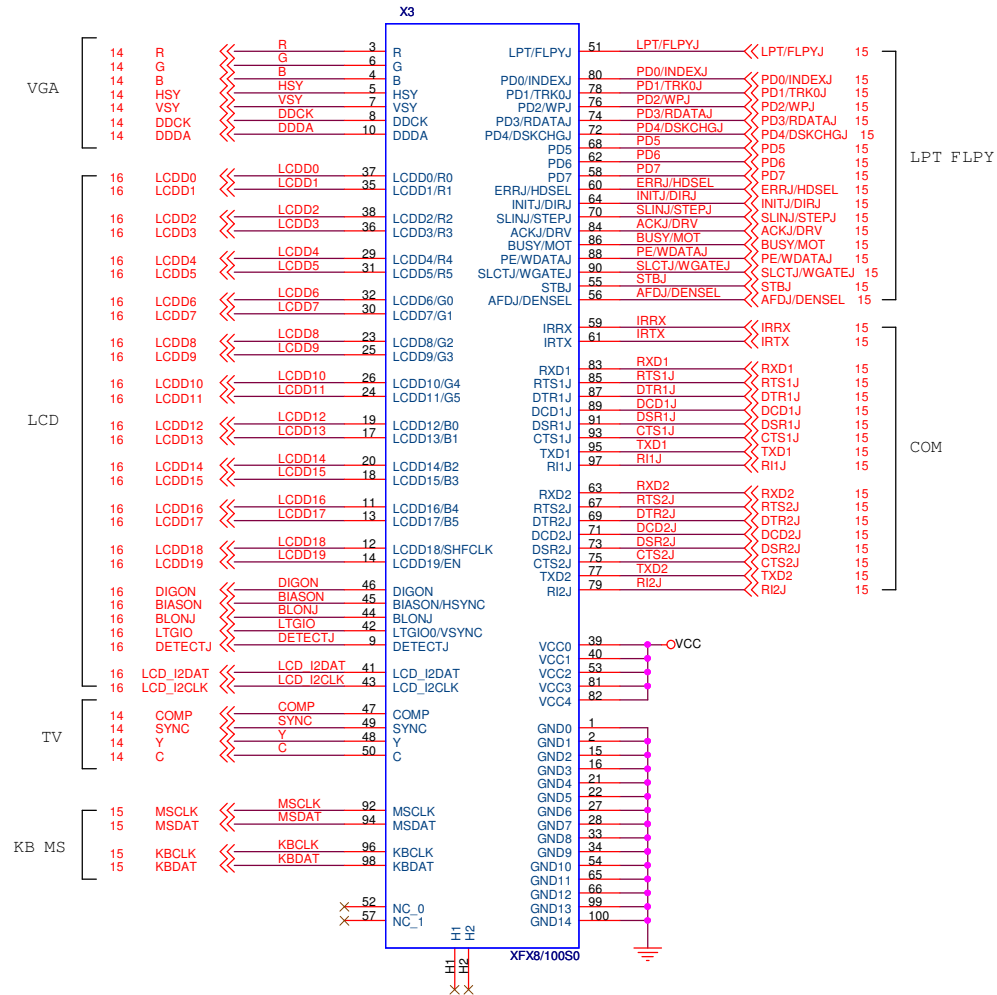
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date: Wednesday, April 05, 2006	Sheet 11	of 21	




**MIC/IN/OUT
opt. 6CH - 5.1**

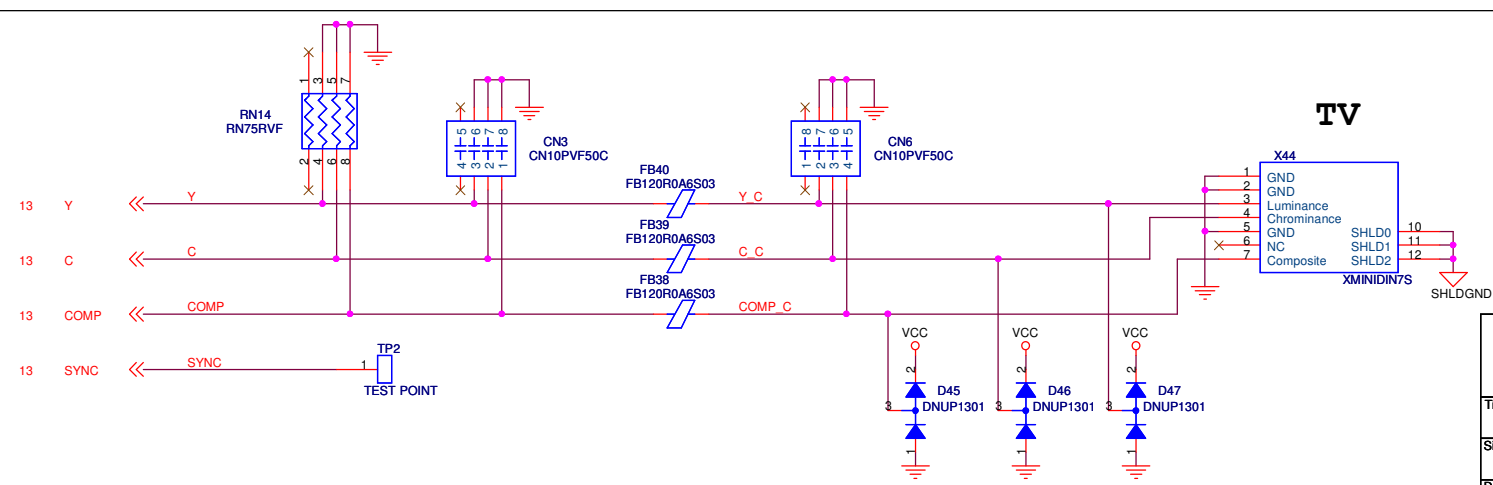
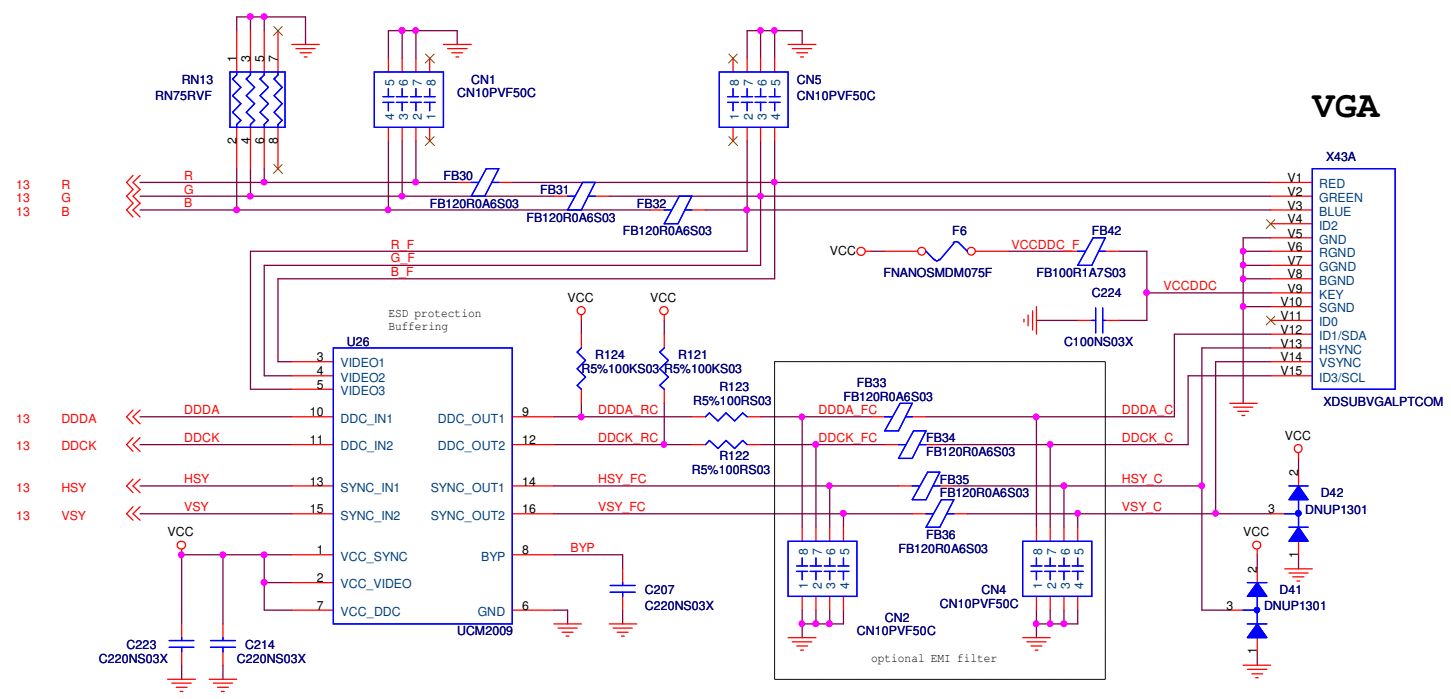
SPDIF


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Title: AC 97			
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date: Wednesday, April 05, 2006	Sheet 12	of 21	

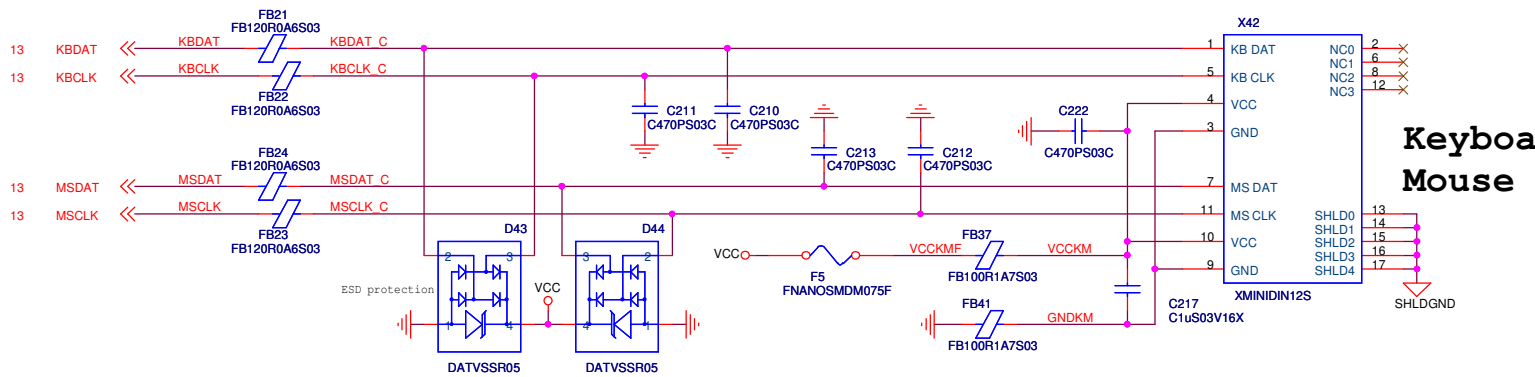
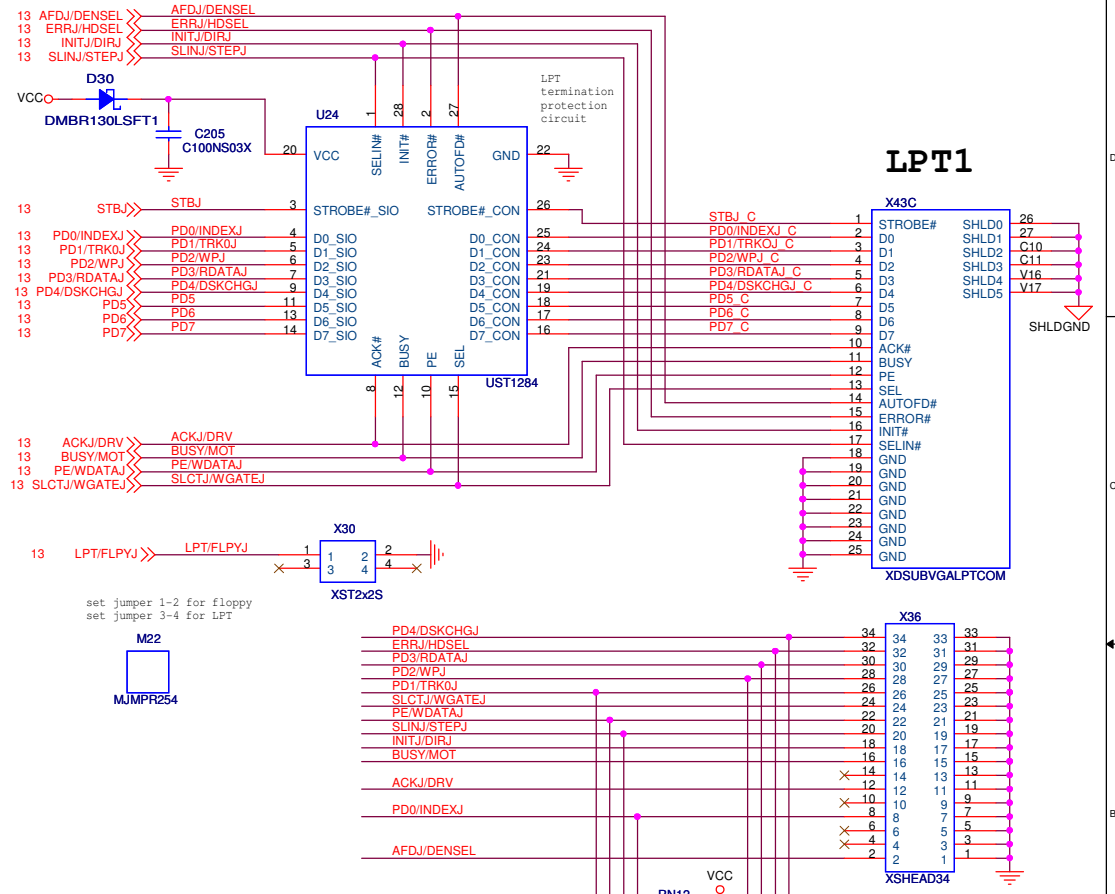
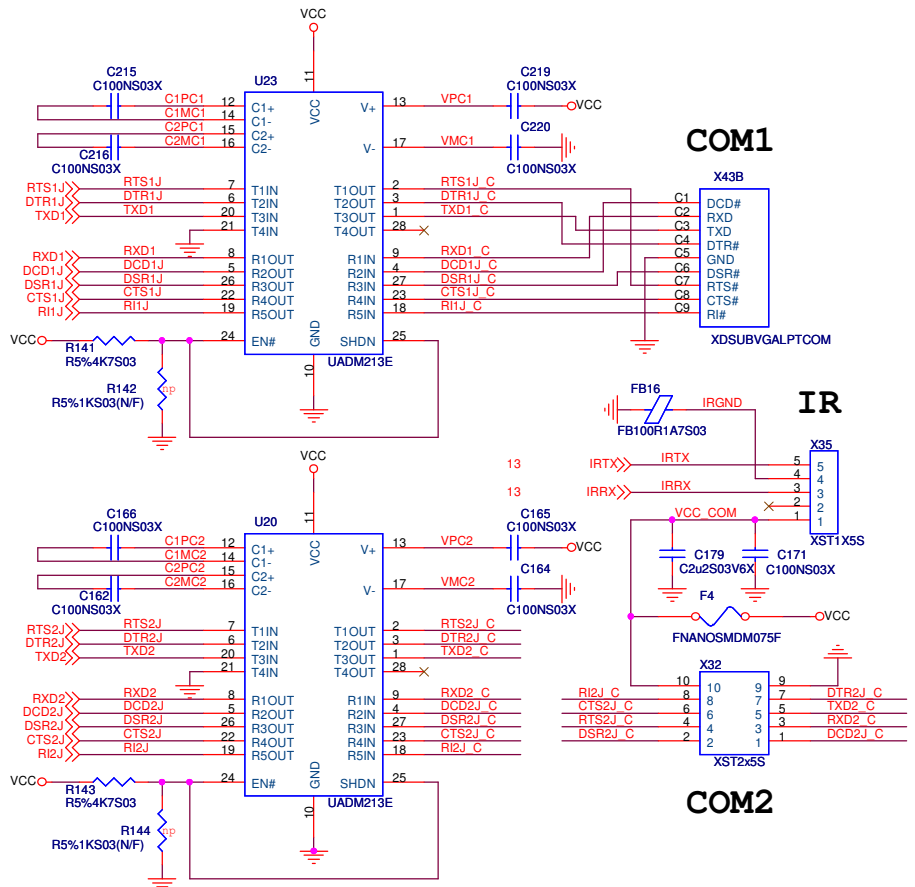


ETX Connector X3
 VGA LCD TV / LPT COM / FLPY / KB MS

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Title XTX Connector X3			
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 13	of 21



 Congatec AG Auwiesenstrasse 5 D-94469 Deggendorf Germany		Title VGA TV	
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 14	of 21



Floppy

This connector supports only 1 drive (A or B)!
2 drives destroy drives and interface!

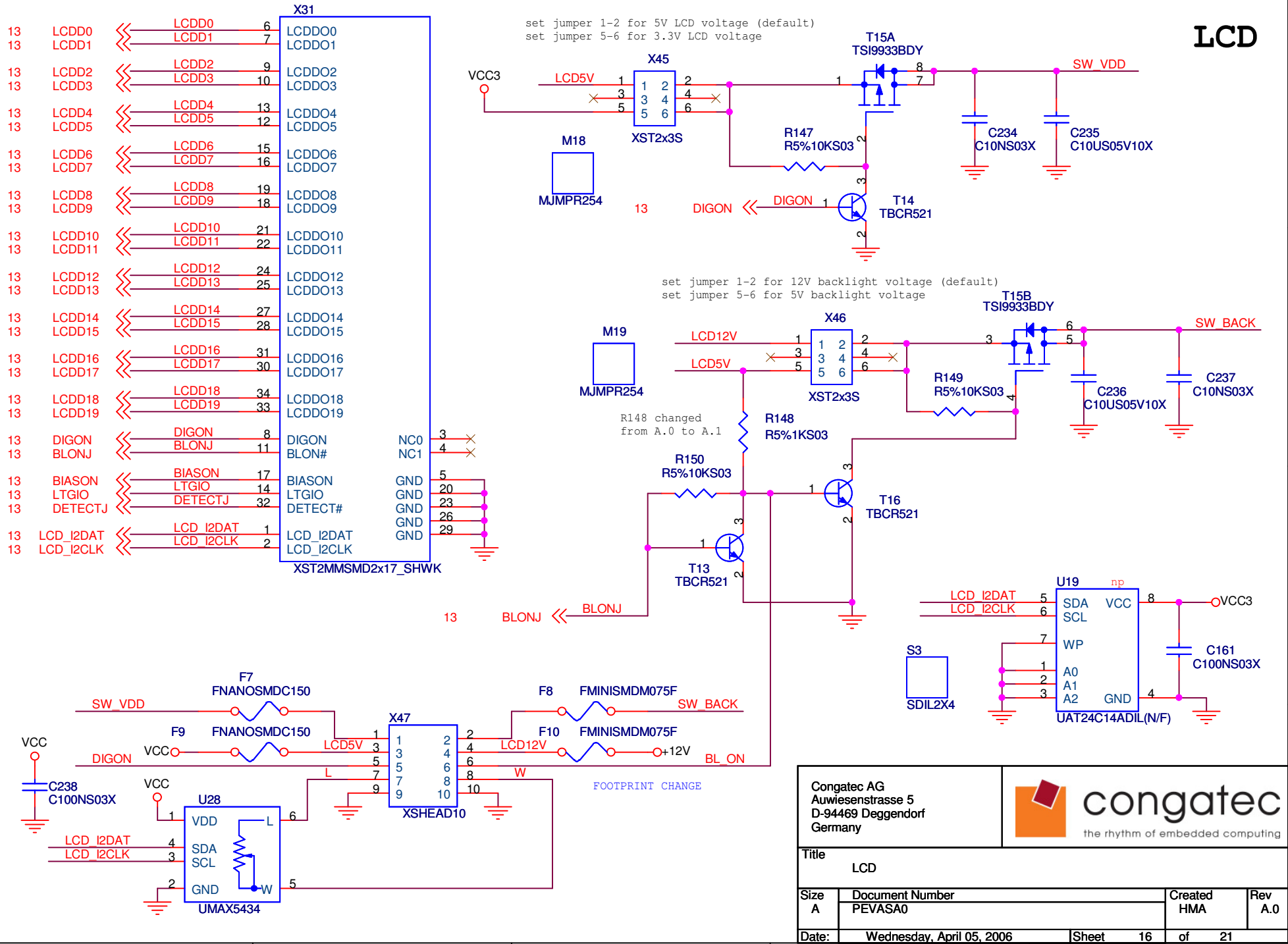
Keyboard Mouse

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Germany


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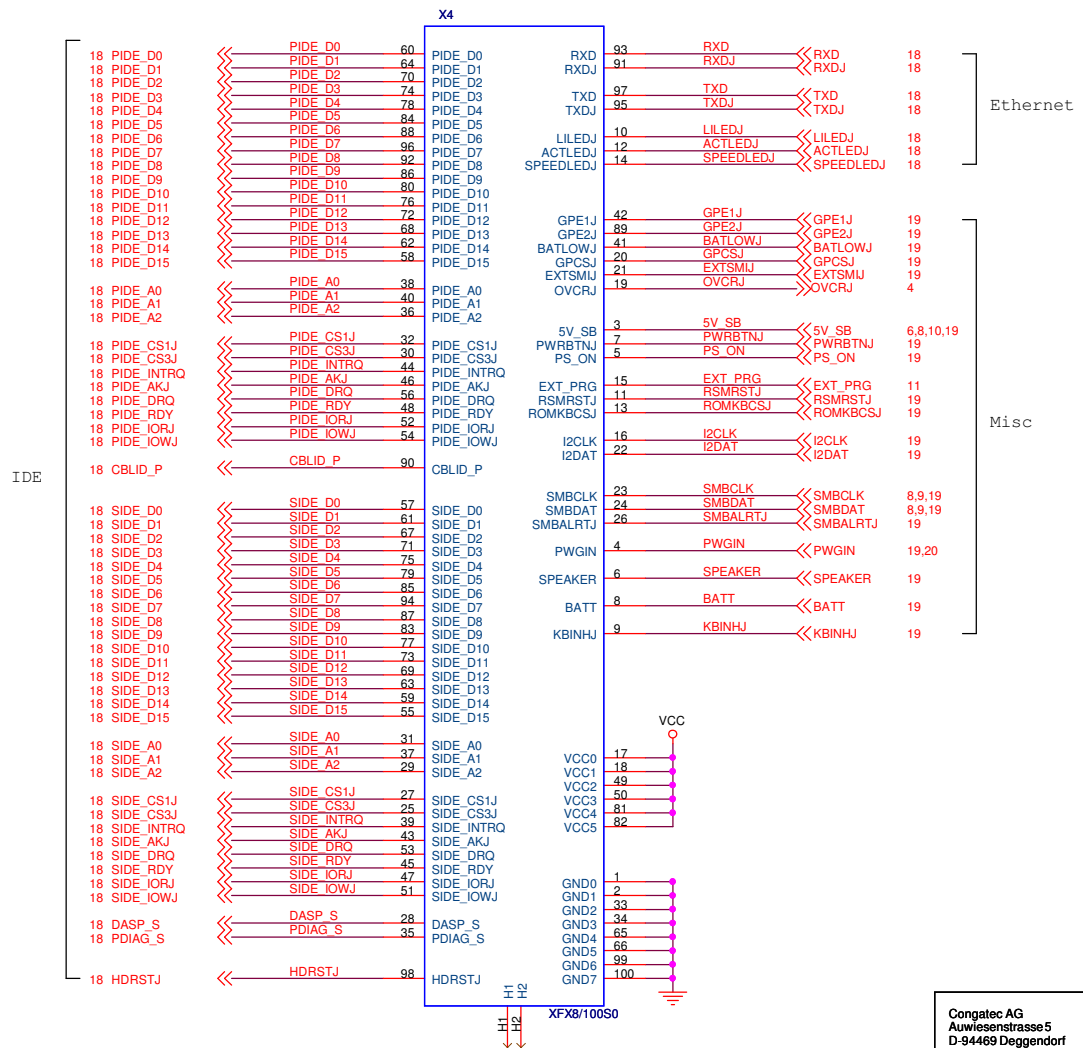
Title			
LPT COM FLPY KB MS			
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 15	of 21

LCD




13	LCDD0	LCDD0	6	LCDD00
13	LCDD1	LCDD1	7	LCDD01
13	LCDD2	LCDD2	9	LCDD02
13	LCDD3	LCDD3	10	LCDD03
13	LCDD4	LCDD4	13	LCDD04
13	LCDD5	LCDD5	12	LCDD05
13	LCDD6	LCDD6	15	LCDD06
13	LCDD7	LCDD7	16	LCDD07
13	LCDD8	LCDD8	19	LCDD08
13	LCDD9	LCDD9	18	LCDD09
13	LCDD10	LCDD10	21	LCDD10
13	LCDD11	LCDD11	22	LCDD11
13	LCDD12	LCDD12	24	LCDD12
13	LCDD13	LCDD13	25	LCDD13
13	LCDD14	LCDD14	27	LCDD14
13	LCDD15	LCDD15	28	LCDD15
13	LCDD16	LCDD16	31	LCDD16
13	LCDD17	LCDD17	30	LCDD17
13	LCDD18	LCDD18	34	LCDD18
13	LCDD19	LCDD19	33	LCDD19
13	DIGON	DIGON	8	DIGON
13	BLONJ	BLONJ	11	BLON#
13	BIASON	BIASON	17	BIASON
13	LTGIO	LTGIO	14	LTGIO
13	DETECTJ	DETECTJ	32	DETECT#
13	LCD_I2DAT	LCD_I2DAT	1	LCD_I2DAT
13	LCD_I2CLK	LCD_I2CLK	2	LCD_I2CLK

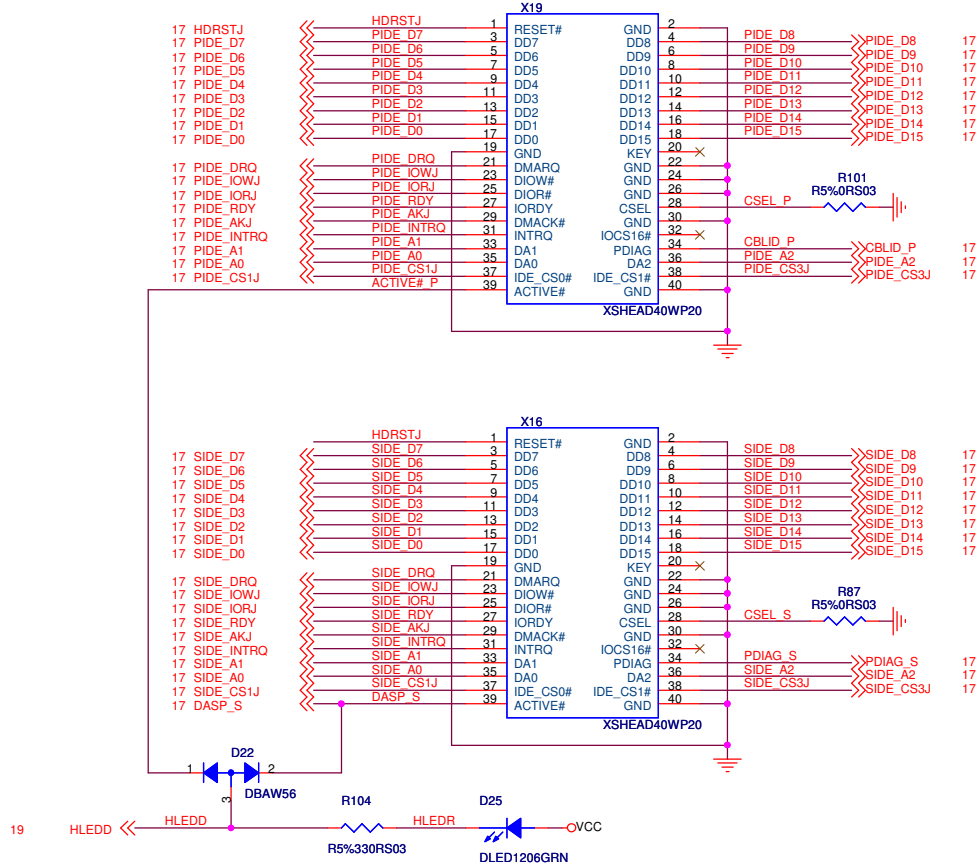
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Size A	Document Number PEVASA0	Created HMA	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 16	of 21



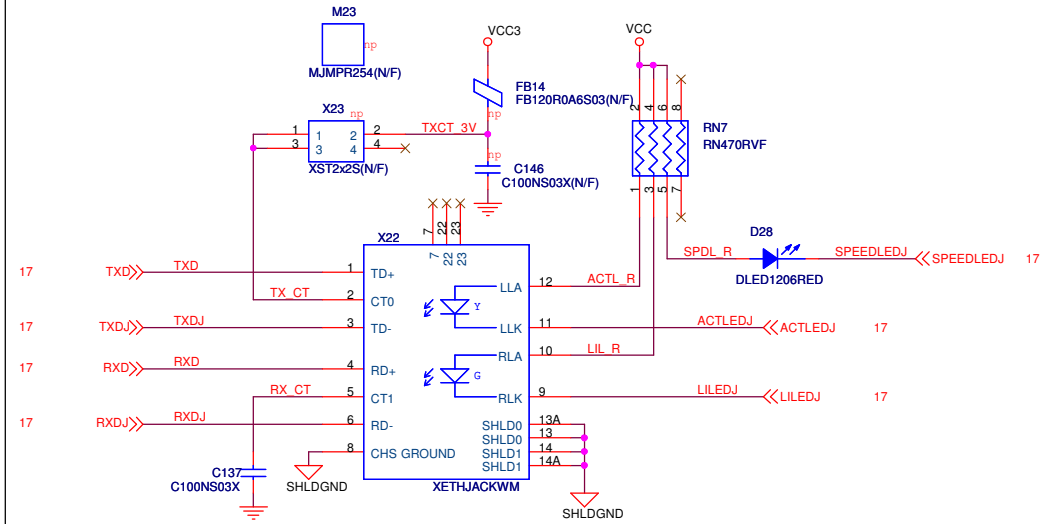
ETX Connector X4
IDE / Ethernet / Misc


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Title: XTX Connector X4			
Size B	Document Number: PEVASA0	Created: HMA	Rev: A.0
Date:	Wednesday, April 05, 2006	Sheet: 17	of: 21

IDE

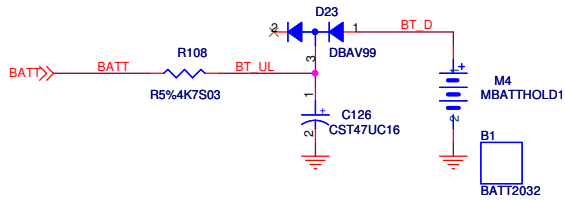


Ethernet

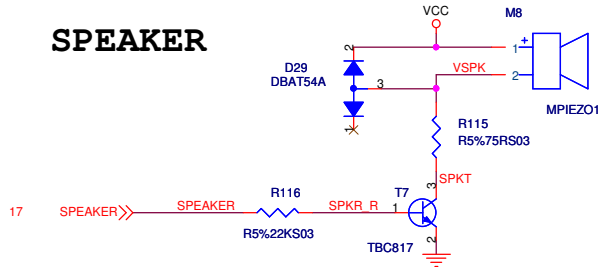


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Date: Wednesday, April 05, 2006	Sheet: 18	of 21	

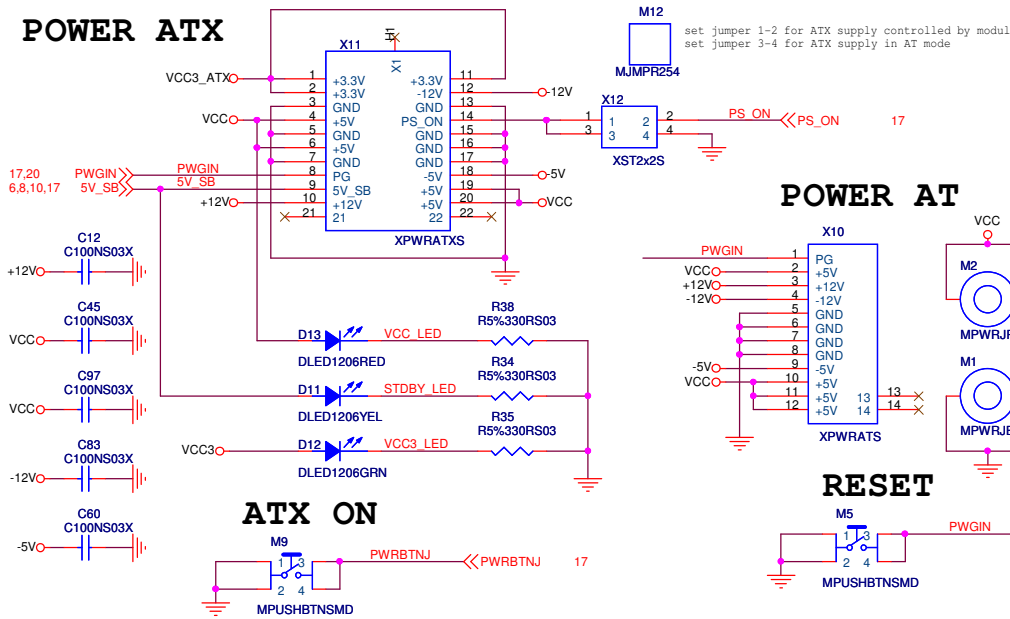
BATTERY



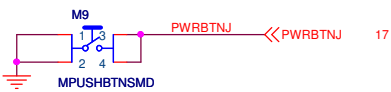
SPEAKER



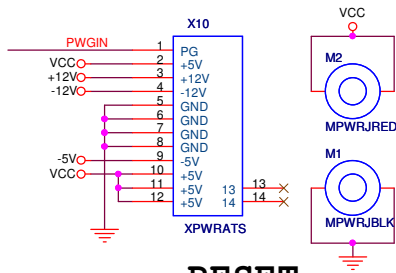
POWER ATX



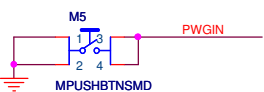
ATX ON



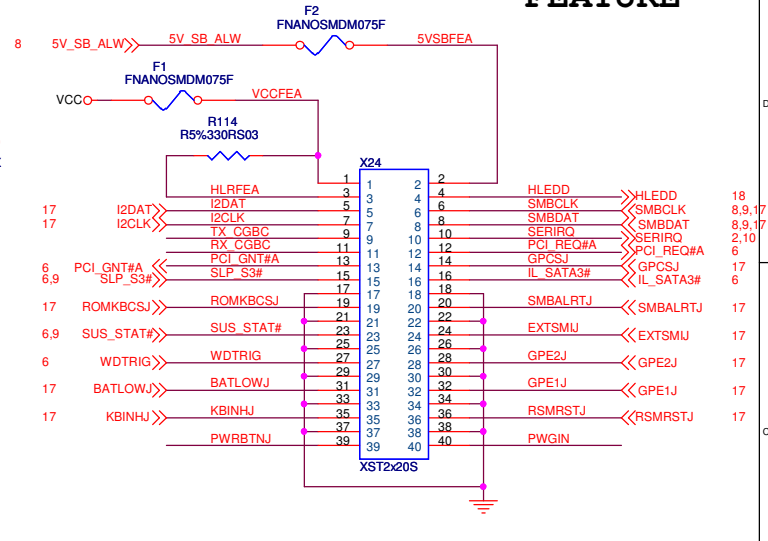
POWER AT



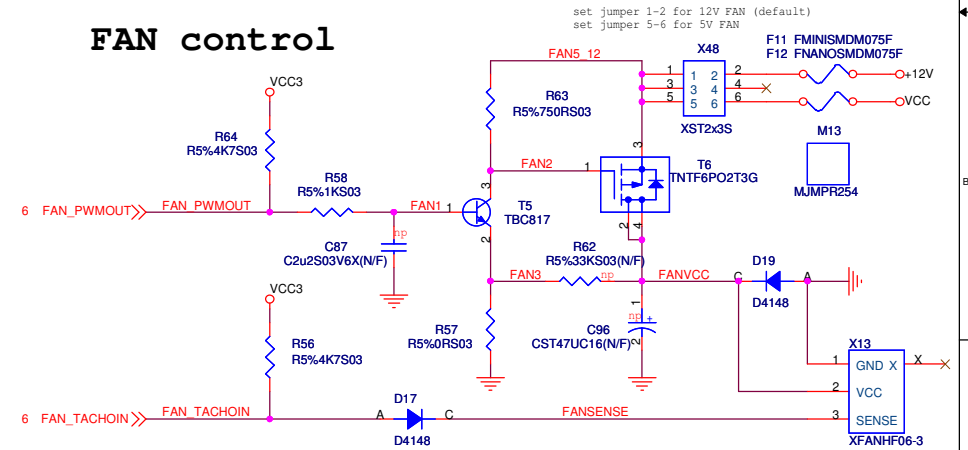
RESET



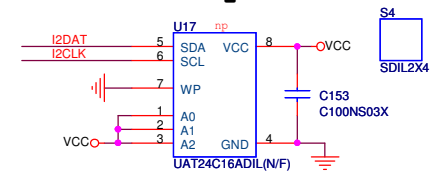
FEATURE



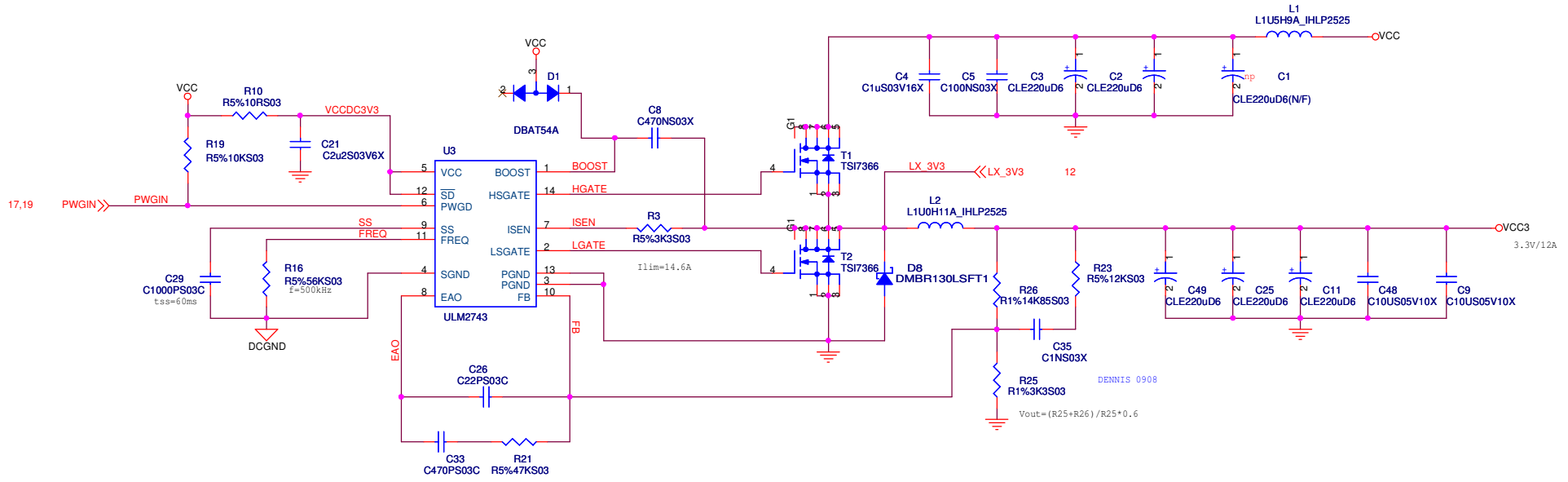
FAN control



I2C Eeprom




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Title: PWR BATT SPK Feature FAN			
Size B	Document Number: PEVASA0	Created HMA	Rev A.0
Date: Wednesday, April 05, 2006	Sheet 19	of 21	



Revision History

Rev. X.0	26.01.05	HMA	0	design created
Rev. X.1	???.?.05	DWG	all	corrected some footprints in PCB
			4	added resistors to use different versions of USB power switches
			8	corrected GND of C82
			10	added resistor to use different versions of serial transmitters
			14	modified pinout of X44
			15	added resistor to use different versions of serial transmitters
			19	added resistor to use different versions of serial transmitters
Rev. A.0	23.05.05	HMA	1	mounting holes for XDVI
			5	exchanged 9536 by 9572 due to complex logic
			6	added resistor jumpers to seperate EXCx_RST# signals
			9	modified ExpressCard Pinout
			10	added optional LPC bus connector for logic analyser
			11	changed connection of D15 to identify S2 as second boot device
				added flashes for BOM reference
			12	changed AC_SDIN0 to AC_SDIN2 on external AC97 codec
			16	new Page; moved LCD interface from page 14 and completly revised
			19	enable of U18 connected to 5V standby
				changed fan voltage connector to prevent shortage (5V/12V) and added fuses for UL
			all	changed some components values

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Title Revision History			
Size B	Document Number PEVASA0	Created HMA	Rev A.0
Date:	Wednesday, April 05, 2006	Sheet 21	of 21